

Energy Harvesting from RF Signals: A Comprehensive Review

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Abstract—Radio Frequency energy harvesting is becoming increasingly important for remotely powered devices. This technology has the capability of converting the received Radio Frequency signal to electricity. For more than two decades there were extensive research activities on energy harvesting system to use it for human utility. In this work we have discussed an extensive literature review on the recent research progresses in radio frequency energy harvesting system.

Indexwords: Radio Frequency Signal, Energy Harvesting, Friss transmission formula.

1. INTRODUCTION

Radio frequency (RF) energy harvesting (EH) system is an active research area which investigates a number of alternative ways to convert energy from electromagnetic (EM) radiation to electricity for energizing the low powered electronic circuits. The RF energy is freely available in the space to use for energizing the low power electronic devices. Therefore, research work has been carried out by many researchers in this field. RF-EH system has the great potential to impact on the cellular phones, portable electronic devices and wireless sensor network (WSN) etc. The RF-EH system needs an efficient antenna along with a circuit capable of converting RF signals to DC voltage. The rectenna is a rectifying antenna which converts the microwave power to DC power and it has received much attention in EH technology. Nowadays RF-EH system has emerged as a promising technology for reducing the slowdown of node energy and network lifetime of WSN.

Besides, RF-EH system is applied in cognitive radio networks, wireless body area network and other charging systems [1]. The energy carrier in RF-EH system is in the range of 3-300 KHz. RF energy sources are of two types:

- Ambient RF sources: This kind of RF sources is freely available and includes most of the radiations from domestic appliances such as TV, Bluetooth, WiFi etc. The frequency range for such type of sources is 0.2-2.4 GHz.
- Dedicated RF sources: This type of dedicated sources is required to recharge nodes that require predictable and high amounts of energy. It is fully controllable and it is

supported for applications with quality of services (QoS). The frequency band used for dedicated energy transmission is from license free industrial, scientific and medical (ISM) band. Due to sharply decreasing RF-DC conversion efficiency at low receives powers; the RF-EH suffers from fading, energy dissipation, path loss etc. The amount of radiated power density at the receiver end in an ES system is given by the Friss transmission formula [2]:

$$P_r = P_t G_t G_r \left(\frac{\lambda}{4\pi R}\right)^2 \quad (2)$$

Where,

P_r : Received power

P_t : Transmitted power

G_t : Transmitted antenna gain

G_r : Received antenna gain

R : Distance between transmitter and receiver

λ : wavelength

The main objective of this work is to discuss various research technologies of RF-EH system. In the next sections of the work, we will discuss in details about the circuit design, techniques used and comparisons of some literatures.

2. OVERVIEW OF TECHNIQUES USED IN RF-EH

In this section, the background and techniques related to RF-EH system will be discussed. The purpose of this section is also to introduce the comprehensive survey of some research works related to the RF-EH circuit design which are discussed within the limit of this work. The system block and the system components of the RF-EH system are shown in **Fig.1** and **Fig.2** respectively.

A. RF sources: The RF sources are divided into dedicated RF sources and ambient RF sources. The dedicated RF sources can use the license free ISM frequency bands for RF energy transfer. Ambient RF sources are divided into static ambient RF sources and dynamic ambient RF sources. The TV and

radio sources are the examples of static ambient RF sources. On the other hand a WiFi access point and licensed user

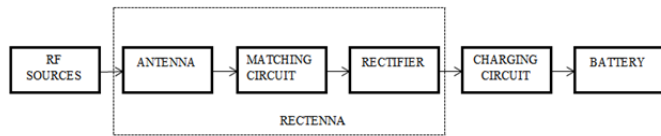


Fig. 1: System block used for RF-EH

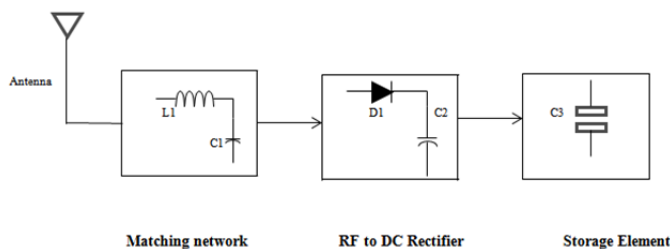


Fig. 2: RF-EH system components in a cognitive radio network are the examples of dynamic ambient RF sources. The authors in [3], [4], [5], [6], [7], and [8] used ambient sources and in [9], [10] dedicated sources are used.

B. Antenna: Antenna is an element of radios and transmitters that aid in the propagation or collection of radio waves. An antenna is simply a copper wire with flowing currents that help to transmit the radio signals further and more efficiently. Electric and magnetic fields are radiated from an antenna and it is responsible for transmission and reception of EM energy through free space. There are various types of antennas used by researchers for the purpose of RF-EH. The authors in [4] used a double-sided microstrip global system of mobile (GSM) embedded antenna which can detect several signals of different frequencies. The bigger antenna can increase power from very low power signals. As the directivity of the antenna increases with its size, and hence increases the gain of the antenna. The gain of the antenna is directly proportional to its directivity.

$$G_0 = e_t D_0 \quad (1)$$

Where D_0 is the directivity of the antenna and e_t is antenna efficiency.

Microstrip antennas have a bandwidth (BW) of 0% up to 5% of their center frequency. Hence the power gathered by his technique is small. An E-shaped single patch antenna was proposed by the authors in [5]. The topology of this antenna was designed on a FR4 substrate and is optimized to capture the energy from ambient at downlink RF range of GSM-900 band. In many research papers, the authors proposed the use of rectenna which is a combination of an antenna and rectifiers. In [11] the authors reported a 2.45 GHz, low power dual circularly polarized and dual access antenna. It contains two

DC recombined rectifiers and a cross slot coupled square patch antenna fed by a microstrip line. A 2.13 GHz rectenna was developed by the authors in [12] for low power mobile devices. The rectenna element is a microstrip patch antenna with polymer thin film transistors (PTFT) board of 10 dielectric constant of 1.6 mm thick that has a gain of 5.8 dBi. A ferrite rod antenna is proposed in [13] which can power a wireless sensor node from ambient medium wave transmission. The coil diameter used for reference in this work was 30 mm and a length of 120 mm. By increasing the diameter of the coil the voltage and power level can be increased. Two compact patch antennas were designed for outdoor RF-EH in powering a soil sensor network as in [9]. The authors in [10] used two kinds of receiver antennas called Dipole and Patch antenna in terms of return loss and peak receiver power. Dipole antenna shows better impedance matching with the conversion circuit than that of the patch antenna. The spiral antenna was used by the authors in [6] to reduce the problem of impedance matching. The authors tried to design the antenna in such a way that the antenna impedance must match to the optimal diode impedance for all frequencies. In [14] it was showed that the dual polarized, dual frequency and dual access antenna show good measurement performances at 940 MHz and 2.45 GHz. It also exhibits harmonic rejection properties at 1.88 GHz and 4.9 GHz avoiding the use of RF filter when the antenna is connected to a nonlinear device. During the last decade ultra wide band (UWB) antenna has also been proposed as a power harvester. In [15] and [16], the use of UWB antenna has been discussed. The authors in [15] used UWB antenna for photovoltaic solar panel integration and EH. The UWB antenna was a transparent cone-top-tapped slot antenna covering the frequency range from 2.2 to 12. GHz. Again in [16] a compact UWB planar antenna fed with a microstrip line is proposed.

C. Matching circuit: Matching circuit is used to transfer maximum power from the antenna to the EH circuit. The authors in [5] used a π matching network to provide a good impedance match for a complex load impedance of $63-j117$ and source impedance 377Ω to transfer maximum power from source to load. The output of the matching network is directly connected to the input of RF to DC converter. In [8], the authors used a simple L network in order to boost the efficiency of RF harvester at low power levels. Simulations of the circuit show the efficiencies of 75% for an input power of 10dBm but in printed circuit board (PCB) design, by actual components, the efficiency decreases below 50%. The impedance of a circuit depends on both frequency and input power. So in [17], the authors first measure the impedance of harvesting circuit without matching circuit and antenna. At a frequency of 866.6 MHz and an input power of -6 dB, the matching was done. The proposed T-match circuit (two series inductors and one parallel capacitor) in [18] measured an impedance of the circuit of $97+j96$ ohm which was matched

with the value of 50 ohm of the antenna. In [19], the authors proposed a method where they used a finite number of discrete capacitors with different capacitance values and the power transfer occurs in a finite number of input power stages. L and π type matching networks are also used in [20] to investigate the RF- EH circuit performances which are shown in Fig.3 and Fig.4. The authors in [20] observed that output voltage of EH circuit using π matching network changes rapidly as compared to L-type matching network as π network has extra frequency dependent component than L network and hence more sensitive to frequency variations.

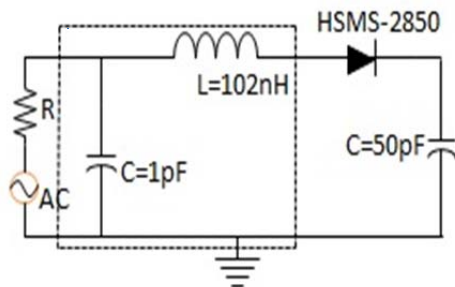


Fig.3: L-type matching network used in [20]

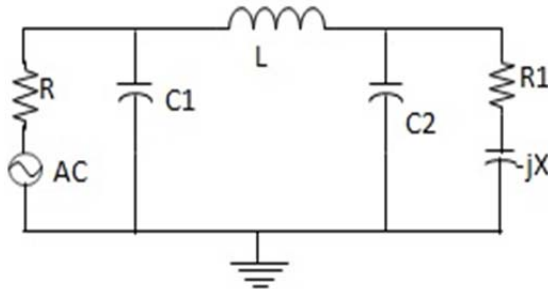


Fig.4: π matching network used in [20]

D. Rectifier: Rectifying circuits are used in EH circuits to convert the AC signals induced in the antenna to DC signals. Schottky diode is mostly preferred rectifying diode for most of the researchers due to its very low forward voltage drop (0.15V) and high switching property. HSMS-2850 is a widely used schottky diode for RF-EH circuits. Single stage rectifiers produce very low efficiency and due to increase in the efficiency and output voltage of the EH circuits, various multistage rectifiers are used by researchers. The authors in [4], [5], [8], [21] etc. have used Villard voltage doubler circuits which is also called Kockcroft - Walton voltage multiplier. In [22] the authors proposed a bridge voltage doubler circuit using active diodes. The authors in [23] proposed a novel highly efficient 5-stage RF rectifier in

Semiconductor manufacturing international corporation (SMIC) 65 nm standard CMOS process. The authors applied the self-compensation approach in this proposed RF rectifier, which combines the gate-bias threshold compensation with the body-effect compensation to improve power conversion efficiency (PCE). The performance of the circuit can be increased by connecting a MOS transistor with the proposed diode [24]. For the compatibility with the CMOS processes a CMOS based Villard voltage multiplier circuit was proposed in [7]. The Greinacher voltage multiplier circuit which was also used by many researchers is also a Villard voltage multiplier with a rectifier circuit connected with it. In Fig.5 and Fig.6 Villard and Dickson voltage multipliers are shown.

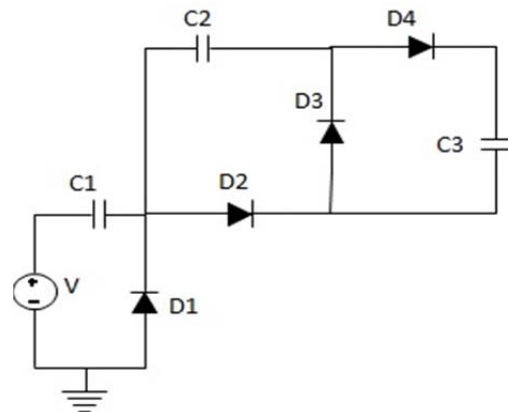


Fig. 5: Villard voltage multiplier in [43]

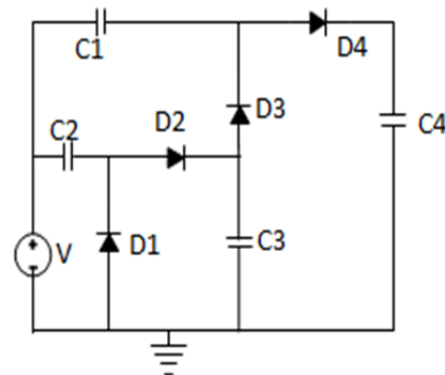


Fig. 6: Dickson voltage multiplier in [43]

3. PERFORMANCE COMPARISON

The comparison of the parameters of a few research workers used in RF-EH circuit are given in Table-I below:

Table-I

Scholar(s)/ Researchers	Techniques used	Output voltage	Efficiency	Receiving antenna
T. R. Ansari et. al (2015)	Schottky diode(HSMS-2822,HSMS-2852) based Greinacher voltage doubler	1.8 V(0dB) 7.7V(10dBm) 17.88V(20dBm)	5.37% at 10 cm distance	Monopole antenna
N. M. Din et. al (2012)	A pi matching network and a 7-stage voltage doubler circuit	2.12 V and 5V (at 0dbm)	N. A	a single wideband 377 ohm E-shaped patch antenna
S. Agrawal et. al (2015)	LNA amplifier with HSMS-2852	-0.92V(-20dBm) -3.7V(-10dBm)	60% (input voltage 100mV)	N.A
M. S. B. Alam et. al (2014)	Greincher/single stage Cockroft-Walton voltage doubler rectifier	N.A	N.A	Microstrip antenna
S. B. Alam et. al (2013)	Single stage voltage multiplier, Greinacher voltage doubler (HSMS-2850)	(0.5-2.0)V	N.A	N.A
N. Akter et.al (2014)	10 stage voltage multiplier with HSMS-2850	5 V(0dBm) 36.06V(20dBm)	N.A	N.A
P. Kaddi et. al (2015)	CMOS technology	1.1V	42%(0.953 GHz) 47%(2.4GHz)	N.A
S. Agrawal et.al (2015)	II and L type matching network	2 V	N.A	N.A
C. J. Ahn et. al (2014)	Step up converter(TPS61220)	0.7-5.5 V	80.9%	Microstrip patch antenna
L. M. Borges et. al (2014)	Five stage Dickson voltage multiplier	N.A	20%(-4dBm) 32%(-6dBm) 26%(-5dBm)	Wearable dual band antenna

4. DISCUSSIONS ON UNADDRESSED PROBLEMS

There are many practical challenges occurred in RF-EH circuits. Some of them are discussed below:

- A high gain antenna is very essential to increase the efficiency of the RF-EH circuits as the harvesting rate is largely affected by the direction and gain of the receiving antenna. This is a major research issue now- a-days.
- There is a need of proper impedance matching circuits which can minimize the loss of power from the antenna to the rectifier circuits. For the impedance mismatch the energy conversion efficiency decreases.
- The RF-EH circuit components should be small enough so that these can be embedded in a low power device.
- As the transmitted power is low in RF-EH circuits, multiple antennas can be adopted to increase the transmission efficiency. Hence power consumption increases with the increase of the number of antenna. But this type of implementation is going to provide problems in the dynamic environment.

5. CONCLUSION

In this work we have presented a comprehensive survey on RF-EH circuit design. First, we have discussed the background of the RF-EH circuit. Then, we have also discussed the techniques used in RF-EH circuits. There are many issues regarding the RF-EH circuit design and those can be modified in further research.

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